

CLK

FIG. 1 (PRIOR ART)

COL_SELECT

FIG. 2 (PRIOR ART)

CAT

FIG. 3 (PRIOR ART)

INT. ADD. VALID

FIG. 4 (PRIOR ART)

T_0 T_1

CLK

FIG. 5 (PRIOR ART)

CMD

WRITE

FIG. 6 (PRIOR ART)

ADDR

COL. ADDR

FIG. 7 (PRIOR ART)

DATA_{min}

FIG. 8 (PRIOR ART)

DQS_{min}

FIG. 9 (PRIOR ART)

DATA_{max}

FIG. 10 (PRIOR ART)

DQS_{max}

FIG. 11 (PRIOR ART)

T_0

T_1

T_2

T_3

T_4

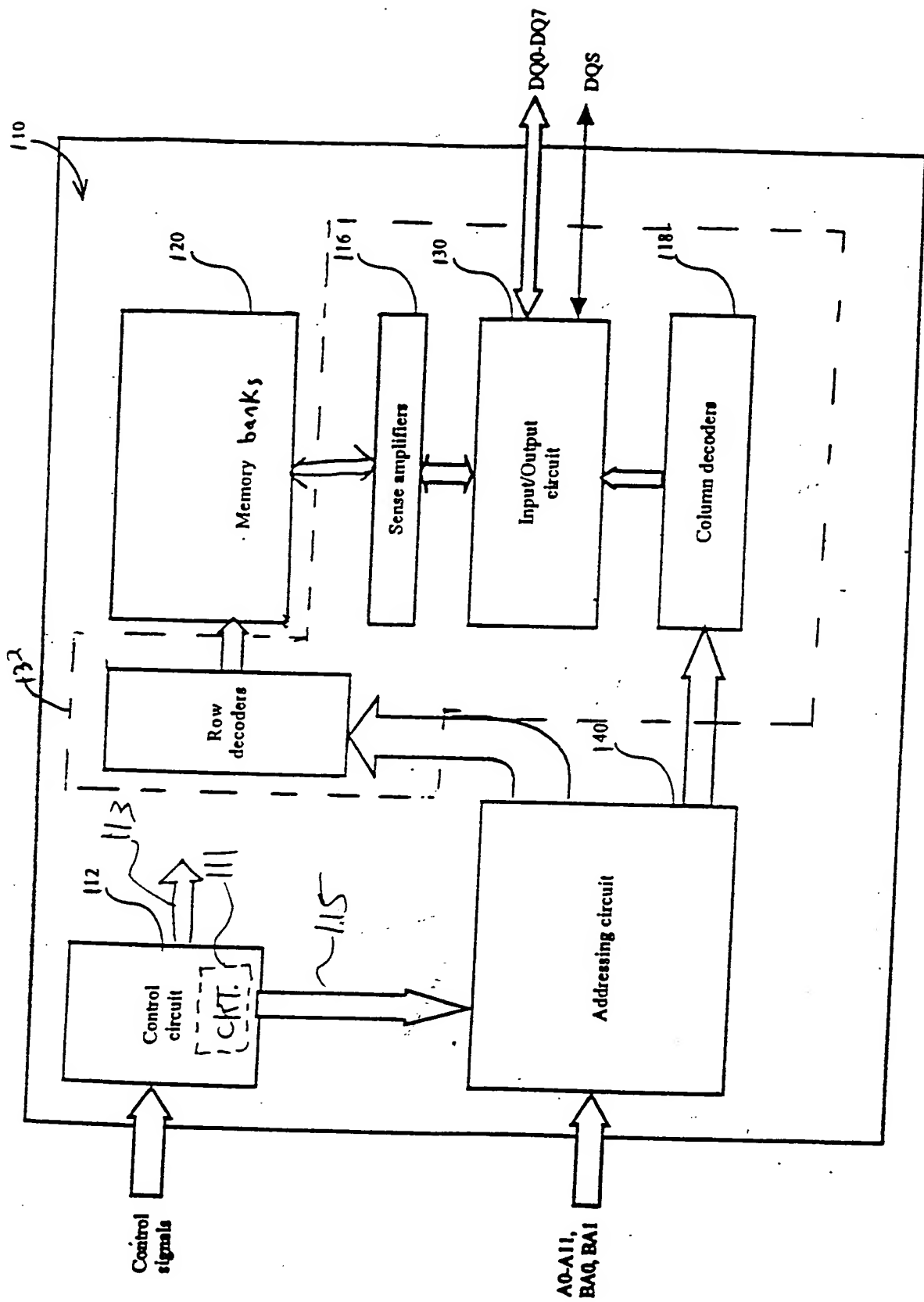
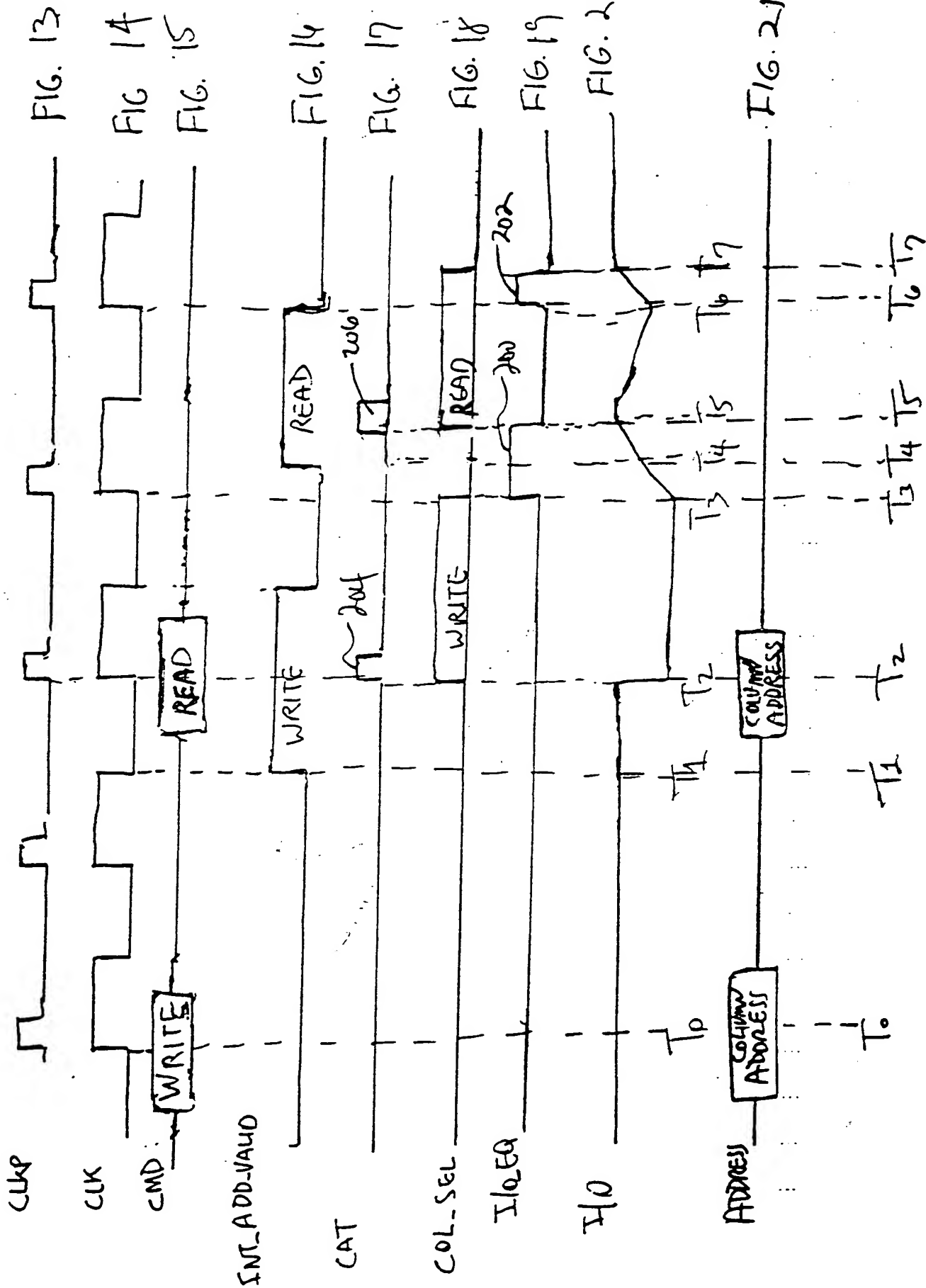


FIG. 12



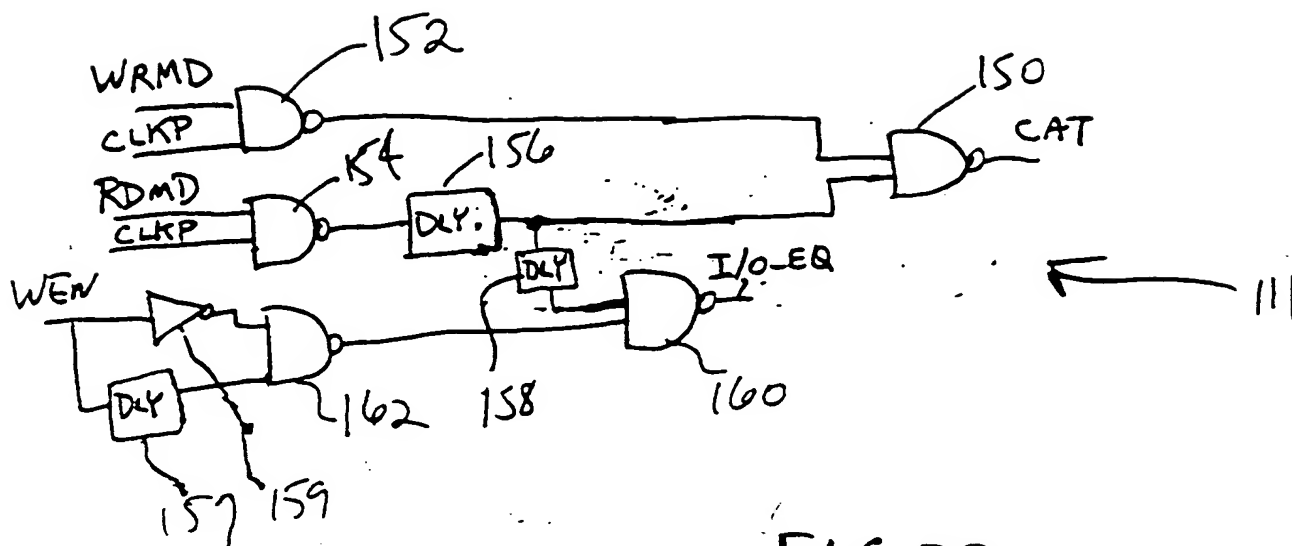


FIG. 22

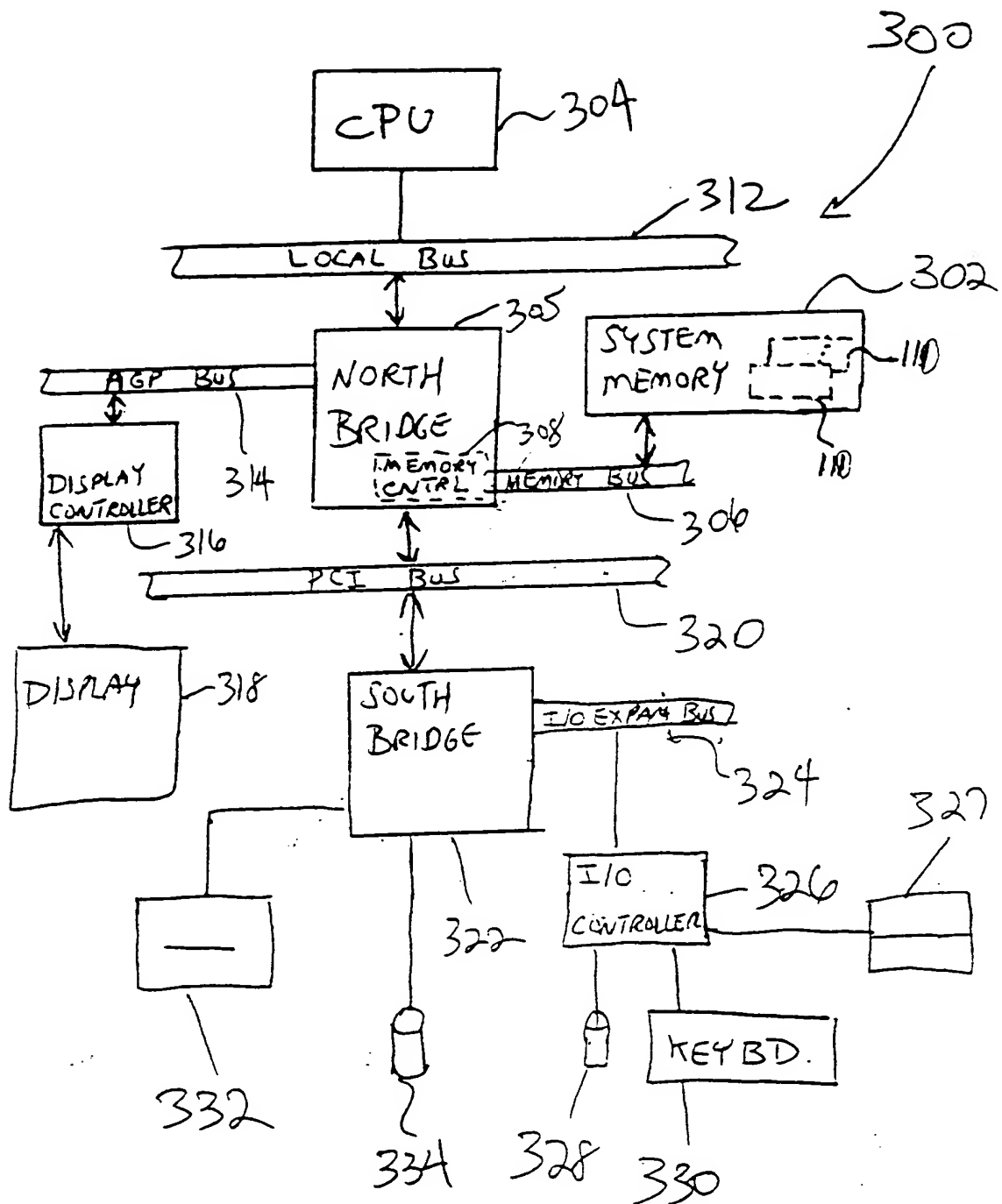


FIG. 23